

68mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R068BS

General Description

The Sanrise SRC60R068BS is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC60R068BS break down voltage is 600V and it has a high rugged avalanche characteristics. The SRC60R068BS is available in TO-247, TO-263-2, TO-220C and TO-220F packages.

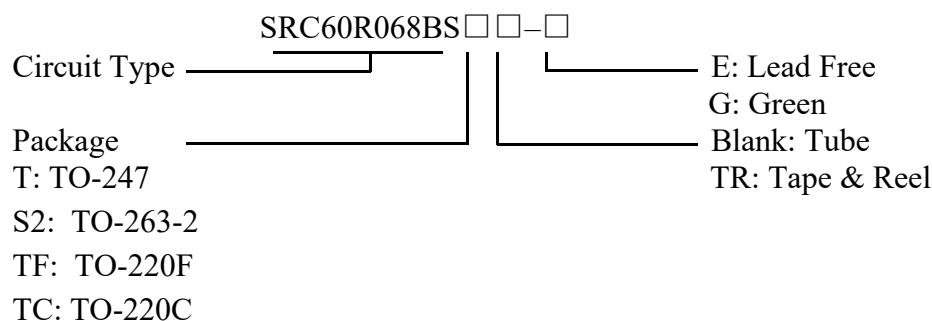
Features

- Ultra Low $R_{DS(ON)}$ = 68mΩ @ V_{GS} = 10V.
- $V_{DS@Tjmax}$ =650v.
- Ultra Low Gate Charge, $Q_g=110nC$ typ.
- Fast switching capability
- Robust design with better EAS performance
- EMI Improved
- Non-automotive Qualified
- Ultra-fast body diode

Application

- Telecom Power
- EV Charger
- High Power Application

Ordering Information



Symbol

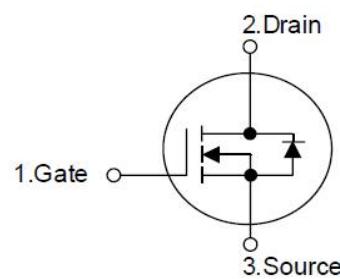
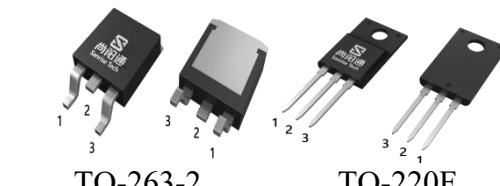
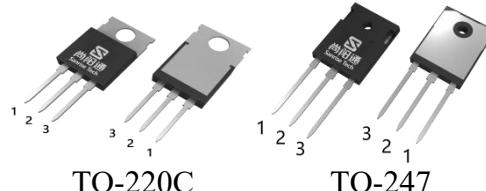


Figure 1 Symbol of SRC60R068BS

Package Type



TO-263-2 TO-220F



TO-220C TO-247

Figure 2 Package Types of SRC60R068BS

Package	Part Number	Marking ID	Packing Type
TO-247	SRC60R068BST-G	SRC60R068BSTG	Tube
TO-263-2	SRC60R068BSS2TR-G	SRC60R068BSS2G	Tape & Reel
TO-220F	SRC60R068BSTF-G	SRC60R068BSTFG	Tube
TO-220C	SRC60R068BSTC-G	SRC60R068BSTCG	Tube

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Absolute Maximum Ratings^{Note1}

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	600	V
Gate-Source Voltage	V _{GSS}	±30	V
Power Dissipation(TO-220C,TO-263-2,TO-247,T _c =25°C)	P _{tot}	357.1	W
Power Dissipation(TO-220F,T _c =25°C)	P _{tot}	35.7	W
Continuous Drain Current	T _c =25°C	48	A
	T _c =100°C	30.3	
	T _c =125°C	21.5	
Pulsed Drain Current (Note 2)	I _{DM}	144	A
Avalanche Energy, Single Pulse (Note 3)	E _{AS}	125	mJ
Avalanche Energy, Single Pulse (Note 5)	E _{AS}	1653	mJ
Avalanche Energy, Repetitive (Note 2)	E _{AR}	0.6	mJ
Avalanche Current, Repetitive (Note 2)	I _{AR}	5.0	A
Continuous Diode Forward Current	I _S	48	A
Diode Pulse Current	I _{S.PULSE}	144	A
MOSFET dv/dt Ruggedness, V _{DS} <=480V	dv/dt	80	V/ns
Reverse Diode dv/dt, V _{DS} <=480V, I _{SD} <=I _D	dv/dt	50	V/ns
Maximum diode commutation speed(Note 4)	diF/dt	1300	A/us
ESD	HBM	>1000	V
Operating Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	T _{LEAD}	260	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I_{AS} = 2.5A, V_{DD} = 60V, R_G = 25Ω, Starting T_J = 25°C. Finish goods test condition.
4. V_{DS}=0...400V, I_{SD}<=30A, T_j=25°C
5. I_{AS} = 7A, V_{DD} = 60V, R_G = 25Ω, Starting T_J = 25°C. Typical Eas.

Thermal characteristics

Parameter	Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	TO-220F	R _{thJC}		3.5	°C /W
	TO-220C			0.35	
	TO-247			0.35	
	TO-263			0.35	
Thermal resistance, Junction-to-Ambient	TO-220F	R _{thJA}		70	°C /W
	TO-220C			58	
	TO-247			58	
	TO-263			58	

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Electrical Characteristics
 $T_J = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	600			V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=600\text{V}, \text{V}_{\text{GS}}=0\text{V}$			10	μA
Gate-Body Leakage Current	Forward	I_{GSSF}	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100	nA
	Reverse	I_{GSSR}	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100	
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=1.0\text{mA}$	3.0	4.0	5.0	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=24\text{A}$		57	68	$\text{m}\Omega$
Gate Resistance	R_G	f=1MHz, Open Drain		1.0		Ω
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$		4.3		nF
Output Capacitance	C_{OSS}			171		pF
Reverse Transfer Capacitance	C_{RSS}			2.8		pF
Effective output capacitance, energy related <small>NOTE6</small>	$\text{C}_{\text{O(er)}}$	$\text{V}_{\text{GS}}=0\text{V},$ $\text{V}_{\text{DS}}=0\ldots 400\text{V}$		94		pF
Effective output capacitance, time related <small>NOTE7</small>	$\text{C}_{\text{O(tr)}}$			550		
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=400\text{V}, \text{I}_D=24\text{A}$ $\text{R}_G=3.3\Omega, \text{V}_{\text{GS}}=10\text{V}$		16		ns
Rise Time	t_r			6.0		
Turn-off Delay Time	$t_{\text{d(off)}}$			98		
Fall Time	t_f			4.0		
Gate Charge Characteristics						
Gate to Source Charge	Q_{gs}	$\text{V}_{\text{DD}}=480\text{V}, \text{I}_D=24\text{A}$ $\text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$		28.1		nC
Gate to Drain Charge	Q_{gd}			56.0		
Gate Charge Total	Q_g			110		
Gate Plateau Voltage	$\text{V}_{\text{plateau}}$			6.5		
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{SD}}=24\text{A}$		0.9	1.1	V
Reverse Recovery Time	t_{rr}	$\text{V}_{\text{R}}=400\text{V}, \text{I}_F=24\text{A}$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$		141		ns
Reverse Recovery Charge	Q_{rr}			0.83		uC
Peak Reverse Recovery Current	I_{rrm}			11.8		A

Note:

 6. $\text{C}_{\text{O(er)}}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 480V

 7. $\text{C}_{\text{O(tr)}}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 480 V



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