$17m\Omega$, 600V, Super Junction N-Channel Power MOSFET

SRC60R017FB

General Description

The Sanrise SRC60R017FB is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC60R017FB break down voltage is 600V and it has a high rugged avalanche characteristics. The SRC60R017FB is available in TO-247 package.

Features

- Ultra Low $R_{DS(ON)} = 17 \text{m}\Omega$ @ $V_{GS} = 10 \text{V}$.
- Vds@Tjmax=650v.
- Ultra Low Gate Charge, Qg=290nC typ.
- Fast switching capability
- Robust design with better EAS performance
- EMI Improved
- Non-automotive Qualified
- Ultra-fast body diode

Application

- Server / Telecom Power
- EV Charger

Symbol

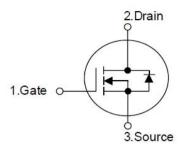


Figure 1 Symbol of SRC60R017FB

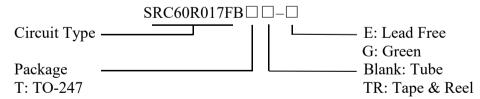
Package Type



TO-247

Figure 2 Package Type of SRC60R017FB

Ordering Information



Package	Part Number	Marking ID	Packing Type	
TO-247	SRC60R017FBT-G	SRC60R017FBTG	Tube	



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Absolute Maximum Ratings

Para	Symbol	Rating	Unit	
Drain-Source Voltage	$V_{ m DSS}$	600	V	
Gate-Source Voltage (static)	V _{GSS}	±20	V	
Gate-Source Voltage (dynamic	V _{GSS}	±30	V	
Power Dissipation (Tc=25°C	P _{tot}	657	W	
	T _C =25°C		120	
Continuous Drain Current	T _C =100°C	I_{D}	76	A
	T _C =125°C		54	
Pulsed Drain Current (Note 2)	I_{DM}	360	A	
Avalanche Energy, Single Pu	Eas	600	mJ	
Avalanche Energy, Single Pulse (Note 4)		E _{AS}	4380	mJ
Avalanche Energy, Repetitive (Note 2)		Ear	0.6	mJ
Avalanche Current, Repetitiv	I _{AR}	5.5	A	
Continuous Diode Forward C	Is	120	A	
Diode Pulse Current	I _{S.PULSE}	360	A	
MOSFET dv/dt Ruggedness,	dv/dt	lt 80		
Reverse Diode dv/dt, V _{DS} <=4	dv/dt	50	V/ns	
Operating Junction Temperate	TJ	150	°C	
Storage Temperature	T_{STG}	-55 to 150	°C	
Lead Temperature (Soldering	TLEAD	260	°C	

Note:

- 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. $I_{AS} = 5.5 A$, $V_{DD} = 60 V$, $R_G = 25 \Omega$, Starting $T_J = 25 ^{\circ} C$. Finish goods test condition.
- 4. $I_{AS} = 14.8A$, $V_{DD} = 60V$, $R_G = 25\Omega$, Starting $T_J = 25$ °C. Typical Eas.

Thermal characteristics

Parameter			Min	Тур	Max	Unit
Thermal resistance, Junction-to-Case	TO-247	R _{thJC}			0.19	°C/W
Thermal resistance, Junction-to-Ambient	TO-247	R _{thJA}			62	°C/W

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Electrical Characteristics

 $T_J = 25^{\circ}C$, unless otherwise specified.

Parameter		Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics		1					
Drain-Source Breakdown Vo	BV _{DSS}	V _{GS} =0V, I _D =250uA	600			V	
Zero Gate Voltage Drain Curr	I_{DSS}	$V_{DS}=600V, V_{GS}=0V$			10	uA	
Gate-Body Leakage Current	Forward	I_{GSSF}	$V_{GS}=30V, V_{DS}=0V$			200	nA
	Reverse	I _{GSSR}	V_{GS} =-30V, V_{DS} =0V			-200	
Gate Threshold Voltage	Gate Threshold Voltage		$V_{DS}=V_{GS}$, $I_D=2.9$ mA	3.0	4.0	5.0	V
Static Drain-Source On-Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =40A		15.1	17	mΩ
Gate Resistance		R_{G}	f=1MHz, Open Drain		1.3		Ω
Dynamic Characteristics		,			•	•	•
Input Capacitance	C _{ISS}	$V_{DS}=400V,$		13.7		nF	
Output Capacitance		Coss	$V_{GS}=0V, f=100KHz$		222		pF
Effective output capacitance, energy related NOTE5		C _{O(er)}	$V_{GS}=0V$,		291		pF
Effective output capacitance, time			$V_{DS}=0480V$		237		
related NOTE6		$C_{O(tr)}$	VDS 0400 V		9		
Turn-on Delay Time	t _{d(on)}			73.2		ns	
Rise Time		$t_{\rm r}$	$V_{DD}=400V, I_{D}=60A$		21.6		
Turn-off Delay Time		t _{d(off)}	$R_G=2\Omega$, $V_{GS}=12V$		184		
Fall Time		t_{f}			12.4		
Gate Charge Characteristic	S						
Gate to Source Charge		Q_{gs}			85		
Gate to Drain Charge		Q_{gd}	$V_{DD}=400V, I_{D}=60A$		90		nC
Gate Charge Total		Qg	$V_{GS}=0$ to $10V$		290		
Gate Plateau Voltage		V _{plateau}			6.5		V
Reverse Diode Characterist	ics						
Drain-Source Diode Forward Voltage		$ m V_{SD}$	$V_{GS}=0V, I_{SD}=40A$		0.85	1.1	V
Reverse Recovery Time		t_{rr}	V -400V I 50A		195		ns
Reverse Recovery Charge		Qrr	V _R =400V, I _F =50A dI _F /dt=100A/us		1.8		uС
Peak Reverse Recovery Current		I _{rrm}			15		A

Note

- 5. C_{O(er)} is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 480V
- 6. $C_{O\,(tr)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 480 V



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